



SRINIVASA INSTITUTE OF ENGINEERING AND TECHNOLOGY (UGC – Autonomous Institution)

(Approved by AICTE, Permanently affiliated to JNTUK, Kakinada, ISO 9001: 2015 certified Institution)
(Accredited by NAAC with 'A' Grade; Recognised by UGC under sections 2(f) & 12(B))
NH-216, Amalapuram-Kakinada Highway, Cheyuru (V), AMALAPURAM -533216.

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Ref No: **SRIN/EEE/BoS/MoM/04**

Date: 24-07-2024

Minutes of Board of Studies Meeting

Minutes of 4th Meeting of Board of Studies (BoS) of Department of Electrical and Electronics Engineering, Srinivasa Institute of Engineering and Technology (Autonomous), NH-216, Cheyuru(V), Amalapuram, held on 23.07.2024(Tuesday), at 10:30 am in its Board Room through Online mode.

Agenda

- To discuss about the curriculum and syllabus of II Year – Electrical and Electronics Engineering students admitted during A.Y. 2023 (R-23).
- To discuss about the curriculum and syllabus offered by – Electrical and Electronics Engineering to other departments.
- Training/Skill Development Programs proposed for Department of – Electrical and Electronics Engineering.
- Minor changes in the syllabus will be done at the level of department by the department Committee members.
- Department Advisory Committee (DAC) and Program Assessment Committee (PAC).
- Results Analysis.
- Any other items with permission of Chair



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Members of the Board of Studies

S.No	Name of the Member	Affiliation of the Member	Contact details	Status
1	Mr.A.V.C Janaki Ramayya	HOD,EEE Department, Srinivasa Institute of Engineering and Technology, Amalapuram-533216	eee@sriniet.edu.in 9848235238	Chairman, BOS- EEE, SIET,AMP.
2	Dr.V.V.N.Murthy	Professor of Electrical and Electronics Engineering, JNTUK, Kakinada, Kakinada-533003	vvmurthy1@gmail.com 9296051421	University Nominee (Nominated by Honorable Vice- Chancellor) JNTUK, Kakinada
3	Dr.M.Jayabharat a Reddy	Professor of Electrical and Electronics Engineering, National Institute of Technology, Trichy Tiruchirapalli- 620015	jbreddy@nitt.edu 9443171746	External Subject Expert (Nominated by Academic Council)
4	Dr.G.Srinivasarao	Professor &HOD, Electrical and Electronics Engineering, & Dean- R&D, Vignan University, Guntur	pa_deanres@vignan.ac .in 9966214663	External Subject Expert (Nominated by Academic Council)
5	Mr.M.RaviKumar	Executive Engineer, APEPDCL, Amalapuram-533216	ee_opn_amp@apeaste rnpower.com 9440812588	Industry Expert
6	Ms S.Naga Sowndarya	Technical Lead HCL Technologies Powai, Mumbai- 400076	sowndarya.sista@gmail .com 9665036961	Alumni Representative (Nominated by the Principal)
7	Dr.S.Jayalakshmi	Dean-Academics, Professor-EEE	deanacademics@sriniet.edu.in 9490323179	Internal subject expert
8	Mr.K.V.V Bapiraju	Associate Professor, EEE Department	kvvbapiraju@sriniet.ed u.in 9866783696	Internal Subject Expert
9	Mr. S.Basavaiah	Assistant Professor, EEE Department	basavaiahsarilla@gmail.com 9014433043	Internal subject expert



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The Chairman of the 4th Board of Studies (BoS), extended a warm welcome to the members present at the venue and connected through online then started the presentation by briefing about the institution and the given agenda was discussed with the members.

Agenda Point	Item	Discussion
Agenda Point- I	<ul style="list-style-type: none">To discuss about the curriculum and syllabus of II Year – Electrical and Electronics Engineering students admitted during A.Y. 2023 (R-23).	<p>The chairman exhibited to all the members the course structure and the syllabus of the following courses has been discussed.</p> <ol style="list-style-type: none">1. Electromagnetic Field Theory2. Electrical Circuit Analysis - II3. DC Machines & Transformers4. Power Systems-I5. Induction And Synchronous Machines6. Control Systems7. Electrical Circuit Analysis-II And Simulation Lab8. DC Machines & Transformers Lab9. Induction And Synchronous Machines Lab10. Control Systems Lab <ul style="list-style-type: none">• The members suggested that replace the Electromagnetic Field Theory with advanced courses or reduce the syllabus for upcoming batches.• The members suggested to relocate the topic distribution system from power systems – I and include this topic after completion of transmission systems if possible.• After observing the course structure members suggested that theory subject having labs are need to plan such that theory in the present semester followed by labs in the next semester or the members proposed to conduct theory classes exclusively related to such type of labs for first few slots before allowing the student to conduct experiment in labs.



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Agenda Point- II	<ul style="list-style-type: none">To discuss about the curriculum and syllabus offered by – Electrical and Electronics Engineering to other departments.	<ul style="list-style-type: none">The members suggested to follow the same syllabus contain.
Agenda Point- III	<ul style="list-style-type: none">Training/Skill Development Programs proposed for Department of – Electrical and Electronics Engineering.	<ul style="list-style-type: none">The members agreed to follow the Training/Skill Development Programs proposed for Department of Electrical and Electronics Engineering
Agenda Point- IV	<ul style="list-style-type: none">Minor changes in the syllabus will be done at the level of department by the department Committee members.	<ul style="list-style-type: none">The members agreed for Minor changes in the syllabus will be done at the level of department by the department Committee members.
Agenda Point- V	<ul style="list-style-type: none">Department Advisory Committee (DAC) and Program Assessment Committee (PAC).	<ul style="list-style-type: none">The members agreed and suggested to maintain Department Advisory Committee (DAC) and Program Assessment Committee (PAC).
Agenda Point- VI	<ul style="list-style-type: none">Results Analysis	<ul style="list-style-type: none">The members discussed about the results and suggested to improve the results by conducting makeup classes for difficult subjects.
Agenda Point- VII	<ul style="list-style-type: none">Any other items with the permission of chair	<ul style="list-style-type: none">It is unanimously resolved to follow the internal and external examination evaluation procedures as per college regulations.



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2	Dr.V.V.N.Murthy	Professor of Electrical and Electronics Engineering, JNTUK, Kakinada, Kakinada-533003	University Nominee (Nominated by Honorable Vice- Chancellor) JNTUK, Kakinada	
3	Dr.M.Jayabharata Reddy	Professor of Electrical and Electronics Engineering, National Institute of Technology, Trichy Tiruchirapalli-620015	External Subject Expert (Nominated by Academic Council)	
4	Dr.G.Srinivasarao	Professor &HOD, Electrical and Electronics Engineering, & Dean-R&D Vignan University, Guntur	External Subject Expert (Nominated by Academic Council)	
5	Mr.M.RaviKumar	Executive Engineer, APEPDCL, Amalapuram-533216	Industry Expert	
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8	Mr.K.V.V Bapiraju	Associate Professor, EEE Department	Internal Subject Expert	
9	Mr. S.Basavaiah	Assistant Professor, EEE Department	Internal Subject Expert	